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1PTO/SB/08A (08-00)

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			Application Number	TBD
			Filing Date	November 21, 2001
			First Named Inventor	Klm
			Group Art Unit	TBD
			Examiner Name	TBD
Sheet 1 of 2	Attorney Docket No.	30699		

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09/991595
10997 U.S. PTO

U.S. PATENT DOCUMENTS						
Exam. Initials*	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Doc.	Date of Pub. of Cited Doc. (mm-dd-yyyy)	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² (if known)			
	AA	5,243,605		Lekmine et al.	09/07/1993	
	AB	5,278,703		Rub et al.	01/11/1994	
	AC	5,321,559		Nguyen et al.	08/14/1994	
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	AF	5,745,315		Aoyama	04/28/1998	
	AG	5,571,127		Reed et al.	06/23/1998	
	AH	5,938,558		Shafiee et al.	08/10/1999	
	AI					
	AJ					

FOREIGN PATENT DOCUMENTS							
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		Application Number	TBD		
		Filing Date	November 21, 2001		
		First Named Inventor	Kim		
		Group Art Unit	TBD		
		Examiner Name	TBD		
Sheet	2	of	8	Attorney Docket No.	30699

OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS			
Exam Initials	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T2
	CA	F. DOLVIO et al., "Fast Timing Recovery for Partial-Response Signaling Systems", IEEE, 1989, pgs. 0573-0577	
	CB	JAKEYUN MOON et al., "Performance Comparison of Detection Methods in Magnetic Recording", IEEE Transactions on Magnetics, Vol. 26, No. 6, November 1990, pgs. 3155-3172	
	CC	HAMID SHAFIEE et al., "Low complexity Viterbi Detection for a Family of Partial Response Systems", IEEE, 1992, pgs. 2892-2894	
	CD	YOUNGGYUN KIM et al., "Delay-Constrained Asymptotically Optimal Detection using Signal-Space Partitioning", IEEE, 1998, pgs. 673-677	
	CE	JAN W. M. BERGMANS et al., "On the Performance of Data Receivers with a Restricted Detection Delay", IEEE Transactions on Communications, Vol. 42, No. 6, June 1994, pgs. 2315-2324	
	CF	TAKUSHI NISHIYA et al., "Turbo-EEPRML: An EEPR4 Channel with an Error-Correcting Post-Processor Designed for 16/17 Rate Quasi-MTR Code", IEEE, 1998, pgs. 2706-2711	
	CG		
	CH		
	CI		
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Examiner Signature	<i>[Signature]</i>	Date Considered	03/07/05
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